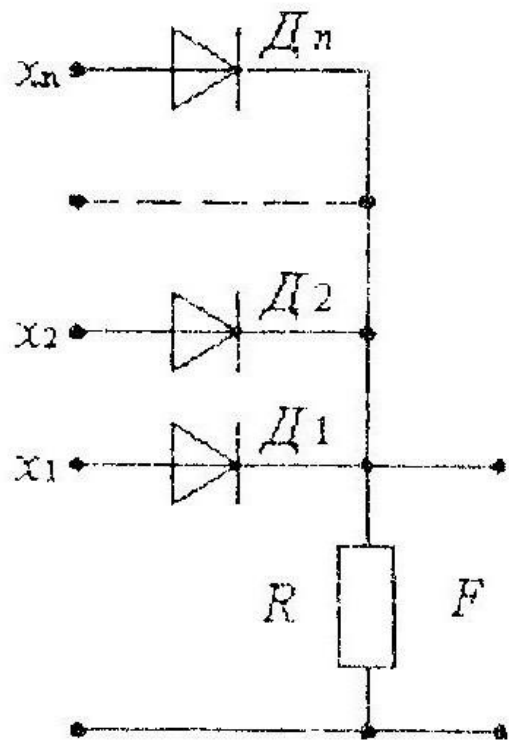
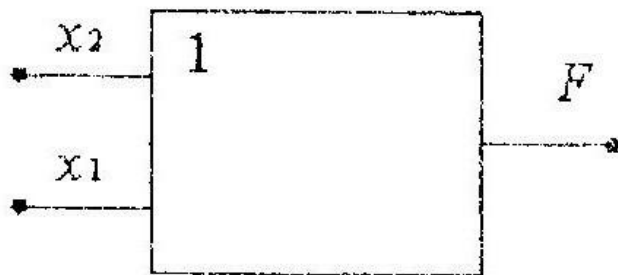


Логические элементы и их схемная реализация

Логический элемент «ИЛИ»



a



б

x_1	x_2	F
0	0	0
0	1	1
1	0	1
1	1	1

в

Логический элемент «И»

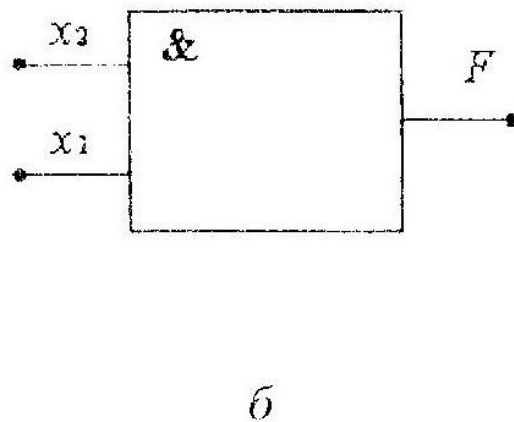
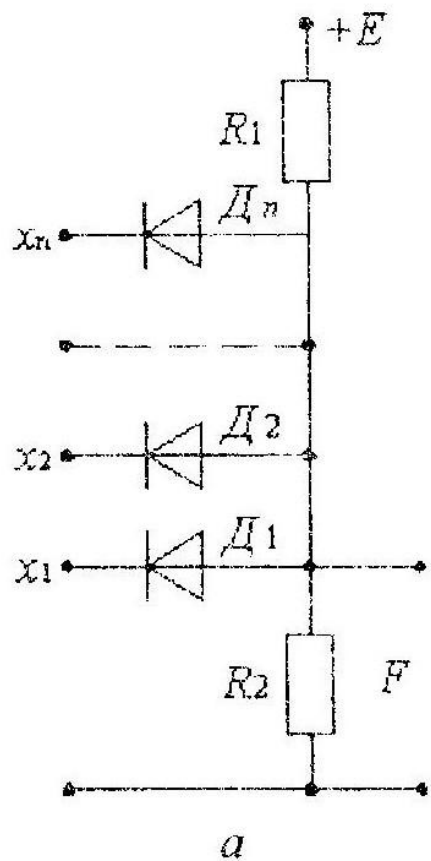
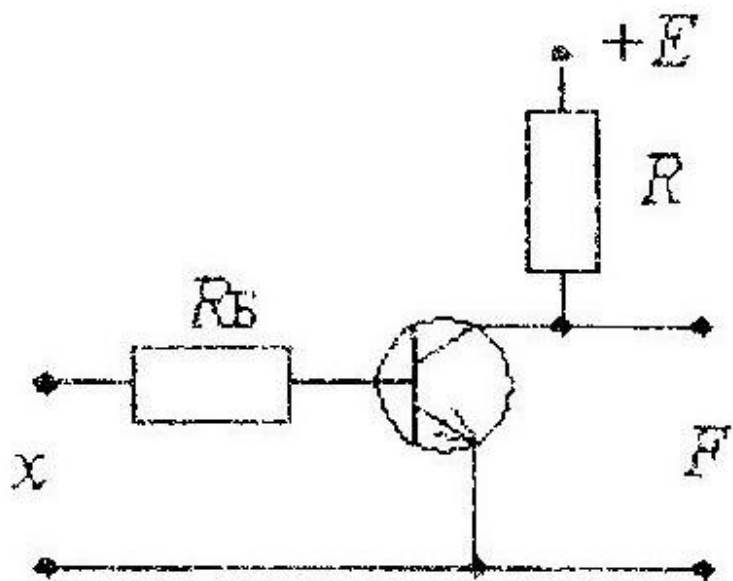


Diagram (в) is a truth table for a 2-input AND gate. The columns are labeled x_1 , x_2 , and F . The rows represent the four possible combinations of input values.

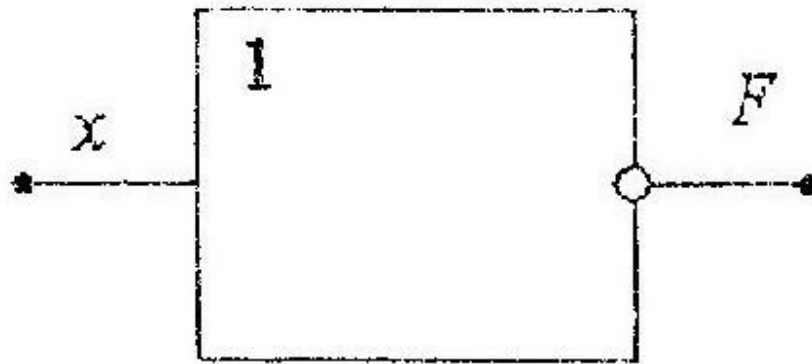
x_1	x_2	F
0	0	0
0	1	0
1	0	0
1	1	1

в

Логический элемент «НЕ»



a

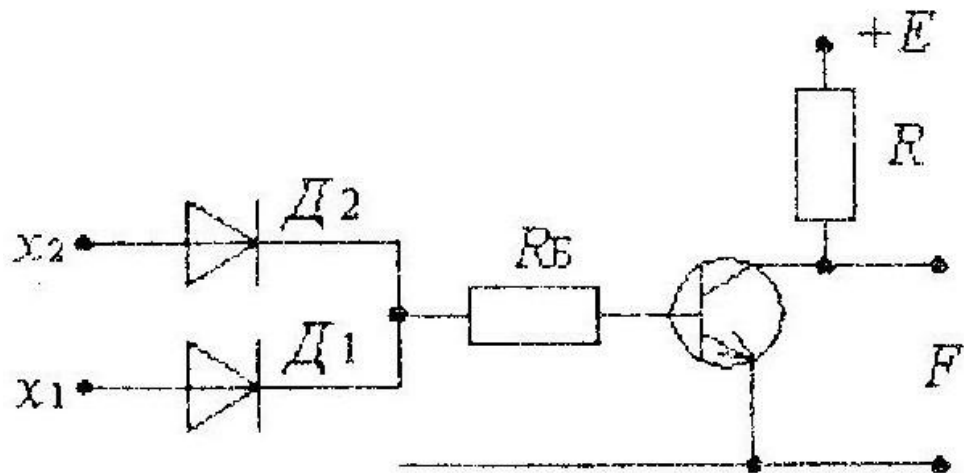


б

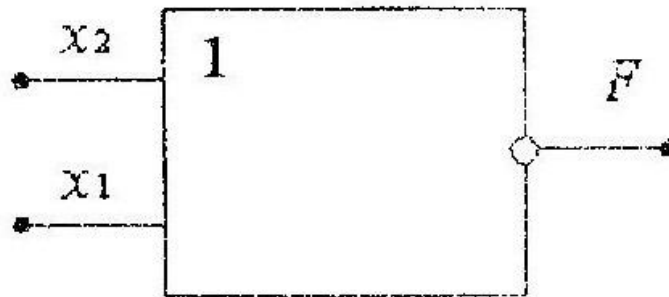
x_1	F
0	1
1	0

в

Логический элемент «ИЛИ-НЕ»



a

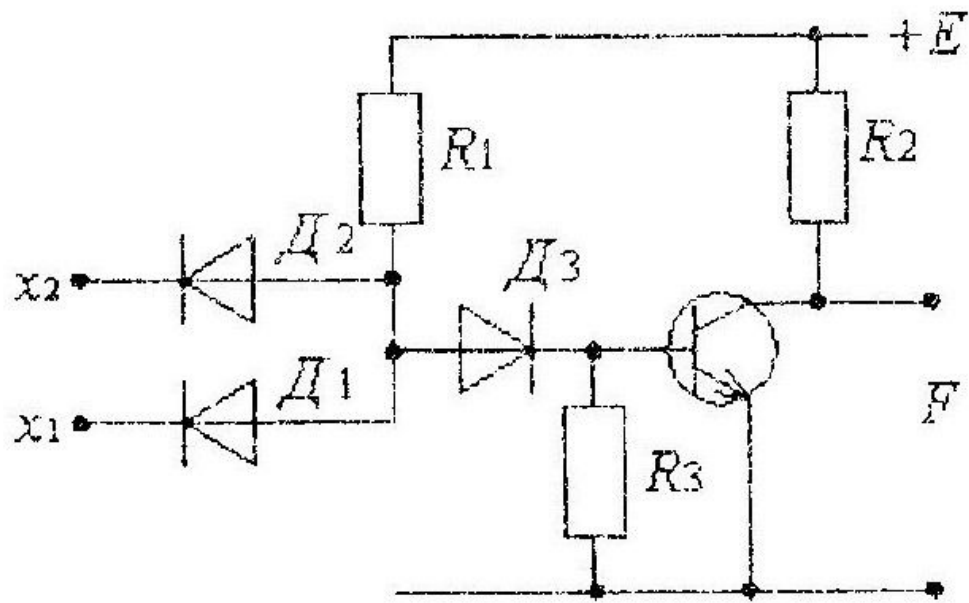


б

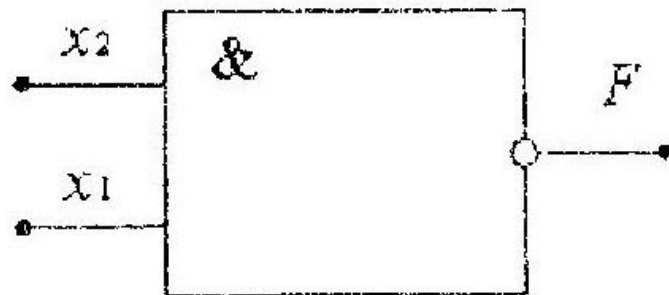
x_1	x_2	F
0	0	1
0	1	0
1	0	0
1	1	0

в

Логический элемент «И-НЕ»



a



б

x_1	x_2	F
0	0	1
0	1	1
1	0	1
1	1	0

в