Fundamentals of Electrical Engineering

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INTRODUCTION

□So far covered: Ohm's law and Kirchhoff's laws

This lecture covers powerful techniques for circuit analysis

nodal analysis - based on a systematic application of KCL

mesh analysis - based on a systematic application of KVL

With these techniques we can analyze any linear circuit by obtaining a set of simultaneous equations that are then solved to obtain the required values of current or voltage

substitution method

lelimination method

Cramer's rule

matrix inversion



Nodal Analysis

In nodal analysis, we are interested in finding the node voltages by applying KCL

- Select a node as the reference node, assign voltages V1, V2, ..., Vn-1 to the remaining nodes, the voltages are referenced 1. with respect to the reference node
- Apply KCL to each of the nonreference nodes, use Ohm's law to express the branch currents in terms of node voltages. 2.
- Solve the resulting simultaneous equations to obtain the unknown node voltages 3.

The reference node is commonly called the ground since it is assumed to have zero potential

$$I_{1} = I_{2} + i_{1} + i_{2} \qquad I_{2} + i_{2} = i_{3}$$

$$I_{1} = I_{2} + i_{1} + i_{2} \qquad I_{2} + i_{2} = i_{3}$$

$$I_{1} = I_{2} + \frac{v_{1}}{R_{1}} + \frac{v_{1} - v_{2}}{R_{2}}$$

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$$I_{2} + \frac{v_{1} - v_{2}}{R_{2}} = \frac{v_{2}}{R_{3}}$$

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$$I_{1} = I_{2} + G_{1}v_{1} + G_{2}(v_{1} - v_{2})$$

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$$I_{1} = I_{2} + G_{1}v_{1} + G_{2}(v_{1} - v_{2})$$

$$I_{2} + G_{2}(v_{1} - v_{2}) = G_{3}v_{2}$$

$$I_{2} + G_{2}(v_{1} - v_{2}) = G_{3}v_{2}$$

$$I_{3} = \frac{v_{2} - 0}{R_{3}} \quad \text{or} \quad i_{3} = G_{3}v_{2}$$

$$I_{1} = I_{2} + G_{1}v_{1} + G_{2}(v_{1} - v_{2})$$

$$I_{2} + G_{2}(v_{1} - v_{2}) = G_{3}v_{2}$$

L



Determine the voltages at the nodes





Nodal Analysis with Voltage Sources

CASE 1: If a voltage source is connected between the reference node and a nonreference node, we simply set the voltage at the nonreference node equal to the voltage of the voltage source $v_1 = 10 \text{ V}$



Supernode CASE 2: If the voltage source (dependent or independent) is connected between two nonreference nodes, the two nonreference nodes form a generalized node or supernode; we apply both KCL and KVL to determine the node voltages

A supernode is formed by enclosing a (dependent or independent) voltage source connected between two nonreference nodes and any elements connected in parallel with it.

$$i_1 + i_4 = i_2 + i_3 \qquad \qquad v_2 - v_3 = 5$$

$$\frac{v_1 - v_2}{2} + \frac{v_1 - v_3}{4} = \frac{v_2 - 0}{8} + \frac{v_3 - 0}{6}$$

$$v_2 - v_3 = 5$$

For the circuit shown find the node voltages.



Find v and i in the circuit



Find v and i in the circuit	V1 = 14 V	42 = 7*V2 + 48 + 8*V2
$V1 = 14V 4 \Omega \qquad V2 \qquad 6V \qquad V3$	6 = V3 - V2	15*V2 = -6
	V3 = 6 + V2	V2 = -6/15 = -0.4 V
$14 V + 3 \Omega \stackrel{>}{\leq} v 2 \Omega \stackrel{>}{\leq} 6 \Omega$	11 = 12 + 13 + 14	V3 = 6 + (-0.4) = 5.6 V
	(14 – V2)/4 = V2/3 + V3/2 + V3/6	I = V3/2 = 5.6 / 2 = 2.8 A
	42 – 3*V2 = 4*V2 + 6*V3 + 2*V3	
	42 = 7*V2 + 8*V3	
	42 = 7*V2 + 8*(6 + V2)	

Mesh Analysis (Planar / Nonplanar)

mesh analysis applies KVL to find mesh currents

Mesh analysis is not quite general because it is only applicable to a circuit that is planar

planar circuit is one that can be drawn in a plane with no branches crossing one another



Mesh Analysis

QA mesh (independent loop) is a loop which does not contain any other loops within it l = b - n + 1

- 1. Assign mesh currents I1, I2, ..., In to the n meshes
- 2. Apply KVL to each of the n meshes. Use Ohm's law to express the voltages in terms of the mesh currents
- 3. Solve the resulting n simultaneous equations to get the mesh currents.

For the circuit, find the branch currents using mesh analysis



 $I_1 = i_1 = 1 \text{ A}, \qquad I_2 = i_2 = 1 \text{ A}, \qquad I_3 = i_1 - i_2 = 0$

Use mesh analysis to find the current IO in the circuit



Use mesh analysis to find the current IO in the circuit







Mesh Analysis with Current Sources



For the circuit, find i1 to i4 using mesh analysis



Nodal and Mesh Analyses by Inspection

Voltage of the selected node multiplied by sum of conductances connected to that node minus all neighboring nodes multiplied with conductance that makes that node neighbor to v_2 the selected one equals to algebraic sum of current sources at the selected node



 $\begin{array}{c|c} (R1+R3)^*i1 - R3^*i2 = v1 \\ (R2+R3)^*i2 - R3^*i1 = -v2 \end{array} \qquad \begin{array}{c|c} R_1 + R_3 & -R_3 \\ -R_3 & R_2 + R_3 \end{array} \qquad \begin{array}{c|c} i_1 \\ i_2 \end{array} = \begin{array}{c|c} v_1 \\ -v_2 \end{array}$

 $\begin{vmatrix} G_1 + G_2 & -G_2 \\ -G_2 & G_2 + G_2 \end{vmatrix} \begin{vmatrix} v_1 \\ v_2 \end{vmatrix} = \begin{vmatrix} I_1 - I_2 \\ I_2 \end{vmatrix}$

 G_3

(G1+G2)*v1 - G2*v2 = I1 - I2(G2+G3)*v2 - G2*v1 = I2

 I_2

 v_1

Current of the selected mesh multiplied by sum of resistances around that mesh minus all neighboring meshes multiplied with resistance that makes that mesh neighbor to the selected one equals to algebraic sum of voltage sources around the selected mesh

Nodal Versus Mesh Analysis

☐ first factor is the nature of the particular network

- Networks that contain many series-connected elements, voltage sources, or supermeshes are more suitable for mesh analysis
- whereas networks with parallel-connected elements, current sources, or supernodes are more suitable for nodal analysis
- □ circuit with fewer nodes than meshes is better analyzed using nodal analysis
- circuit with fewer meshes than nodes is better analyzed using mesh analysis
- second factor is the information required
 - □ If node voltages are required, it may be better to apply nodal analysis
 - If branch or mesh currents are required, it may be better to use mesh analysis

Home Work

From book Fundamentals of Electric Circuits (FIFTH EDITION) by Charles K. Alexander and Matthew N. O. Sadiku solve:

Chapter 3, Problems section (pages 114-124):

Problems – 3.1, 3.2, 3.10, 3.11, 3.15, 3.16, 3.33, 3.36, 3.41, 3.44, 3.45, 3.49, 3.51