

W. Oberschelp/G. Vossen

Rechner- aufbau und Rechner- strukturen

10. Auflage



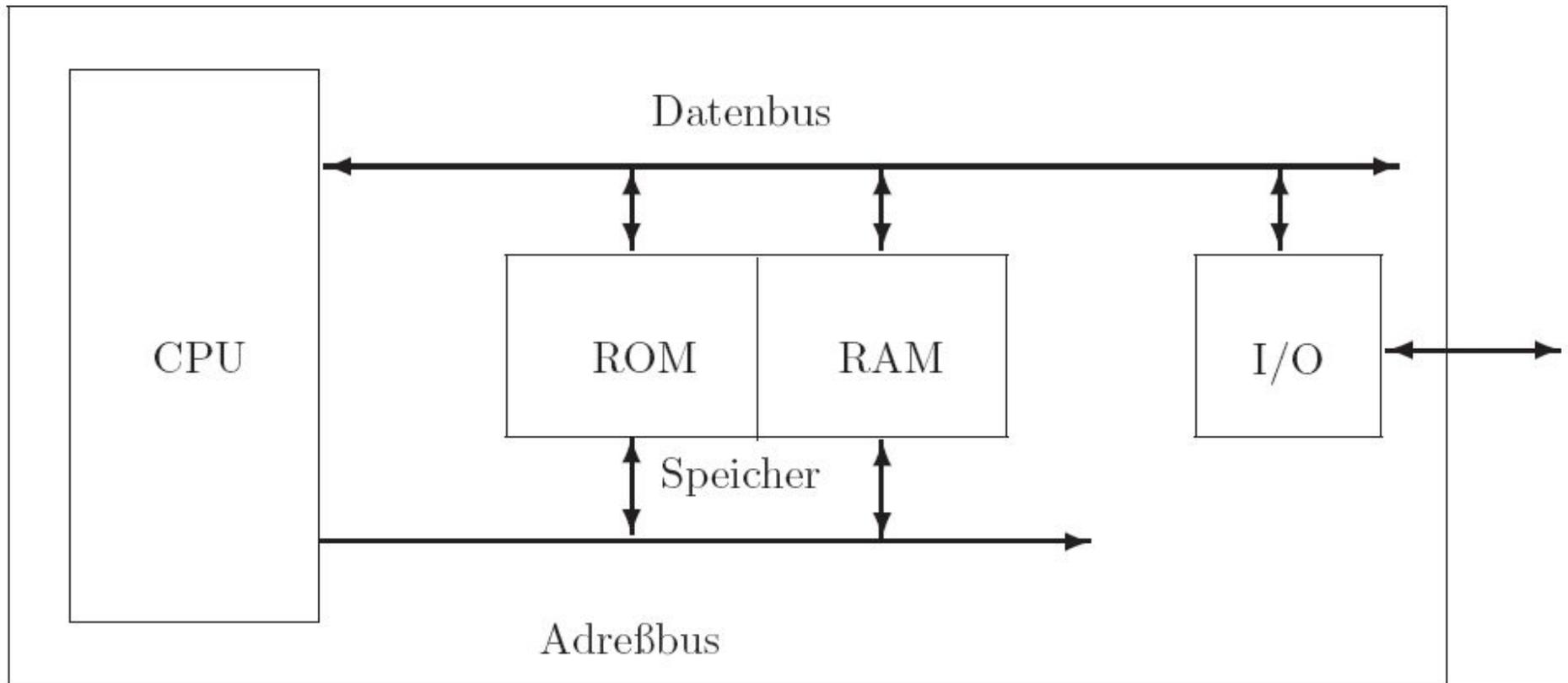
Oldenbourg

Kapitel 8:
Organisationsplan
eines
Von-Neumann-Rechn
ers

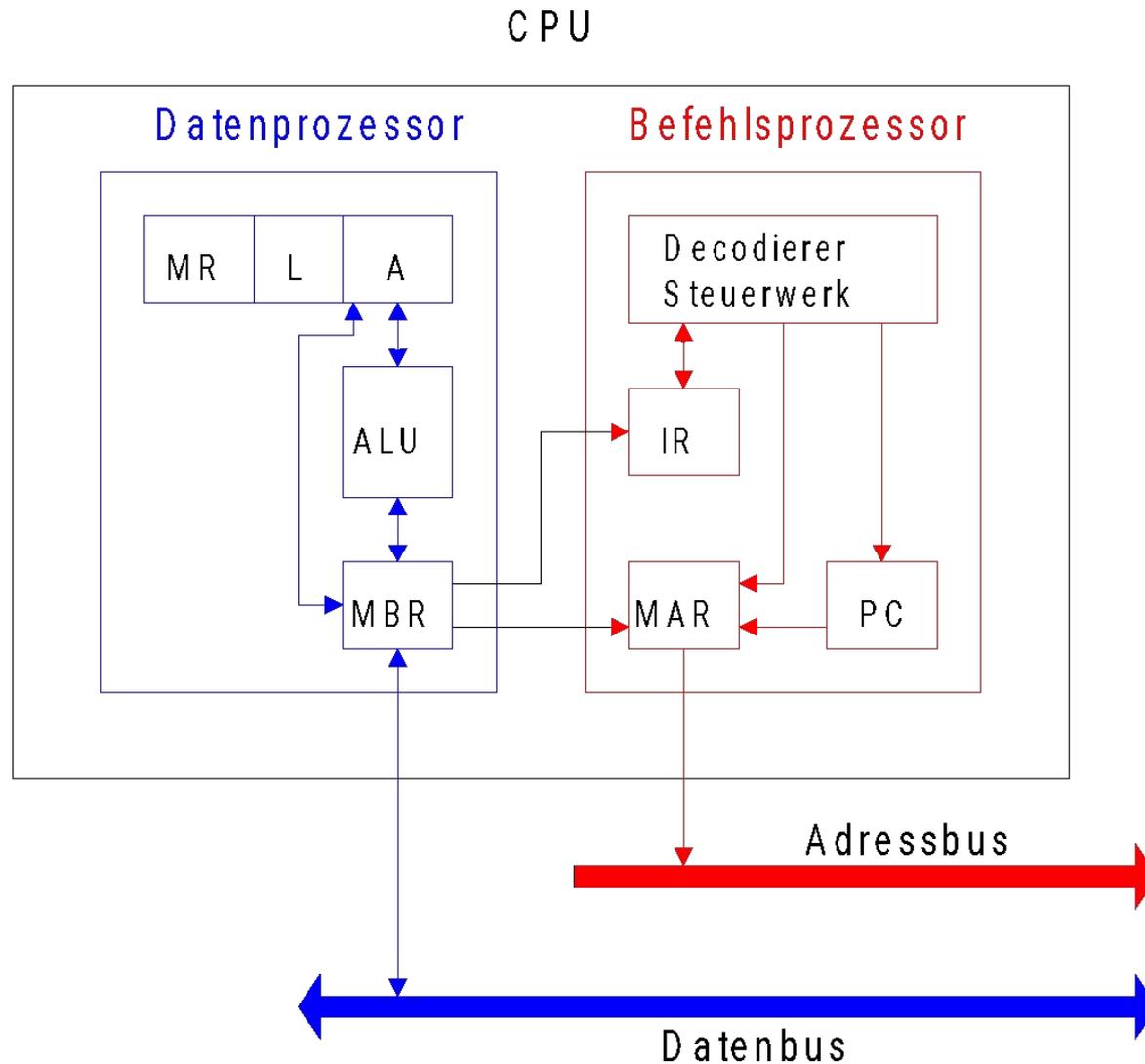
Übersicht

- Einführung
- Die Arbeitsweise einer Zentraleinheit (CPU)
- Der Speicher
- Die E/A-Einheit. Das Konzept des Interrupts
- Busse
- Klassifikation von Von-Neumann-Rechnern

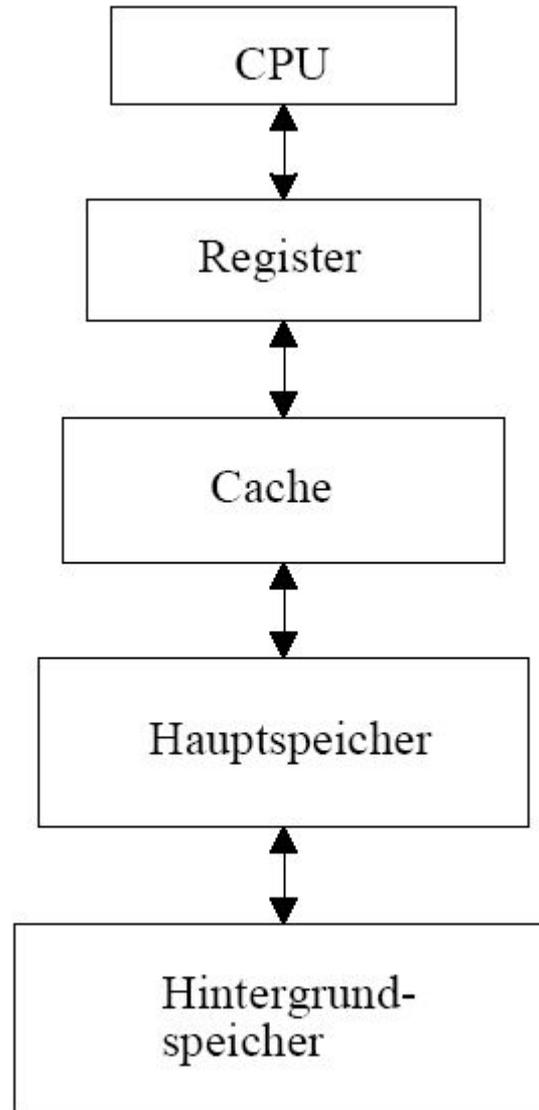
8.1 Struktur eines Von-Neumann-Rechners.



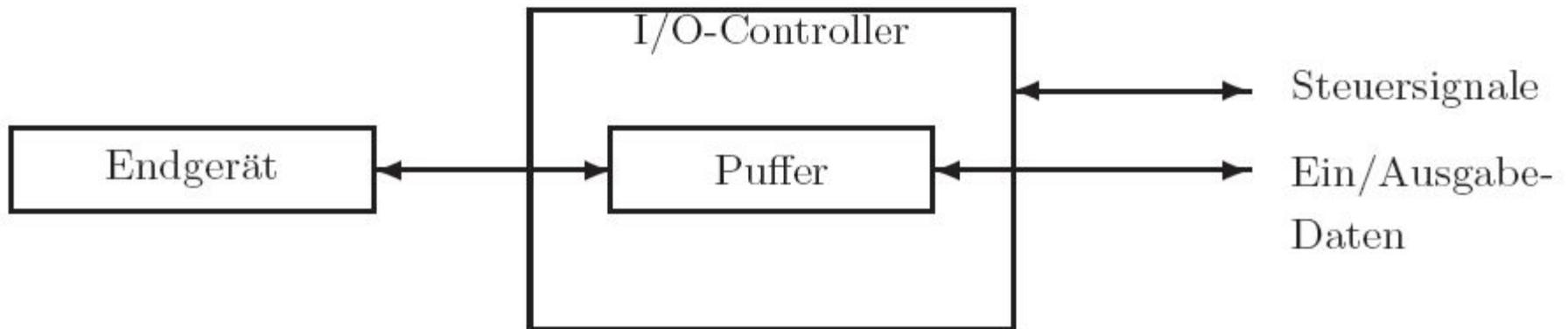
8.2 Struktur einer CPU.



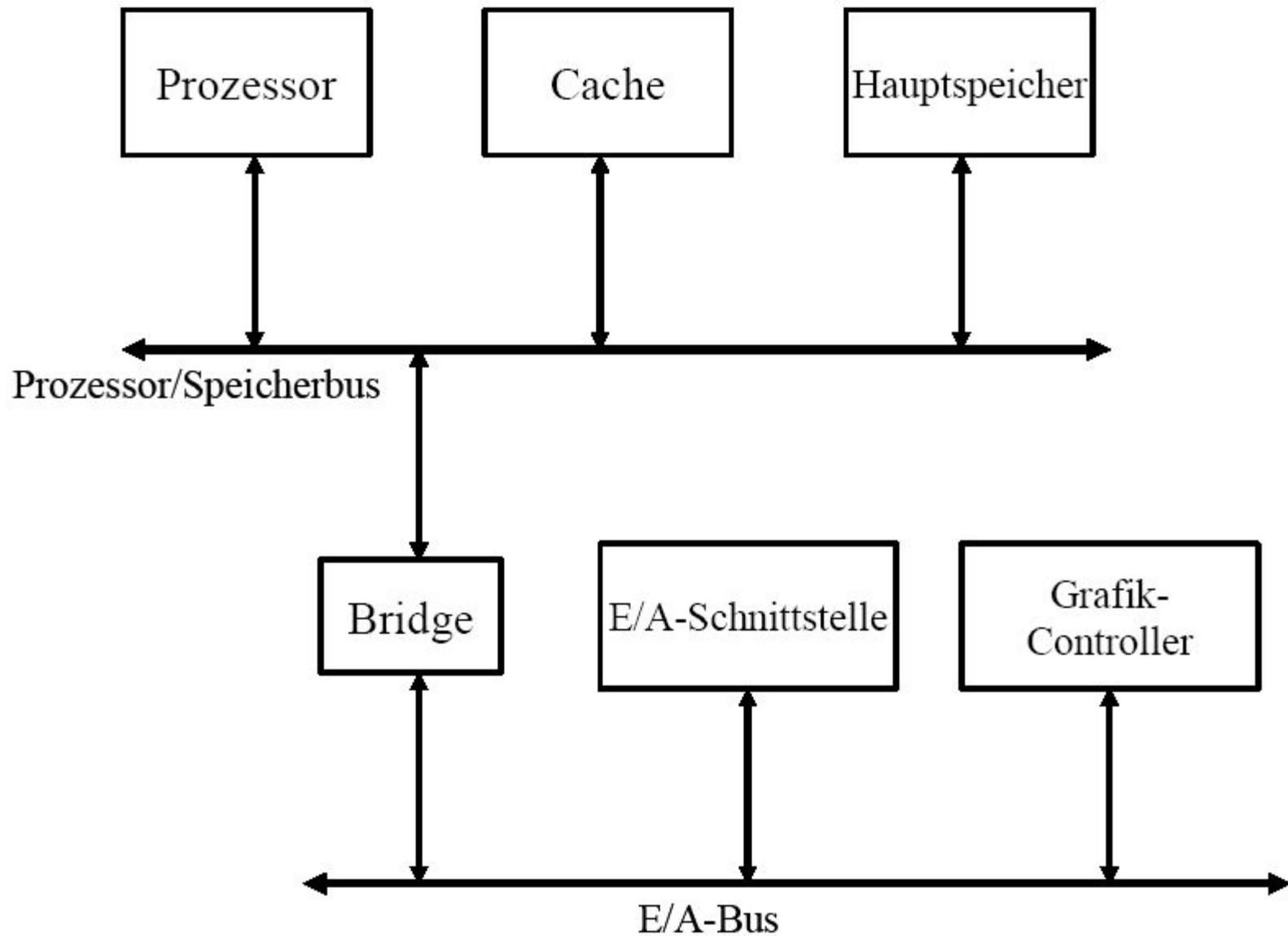
8.3 Speicherhierarchie.



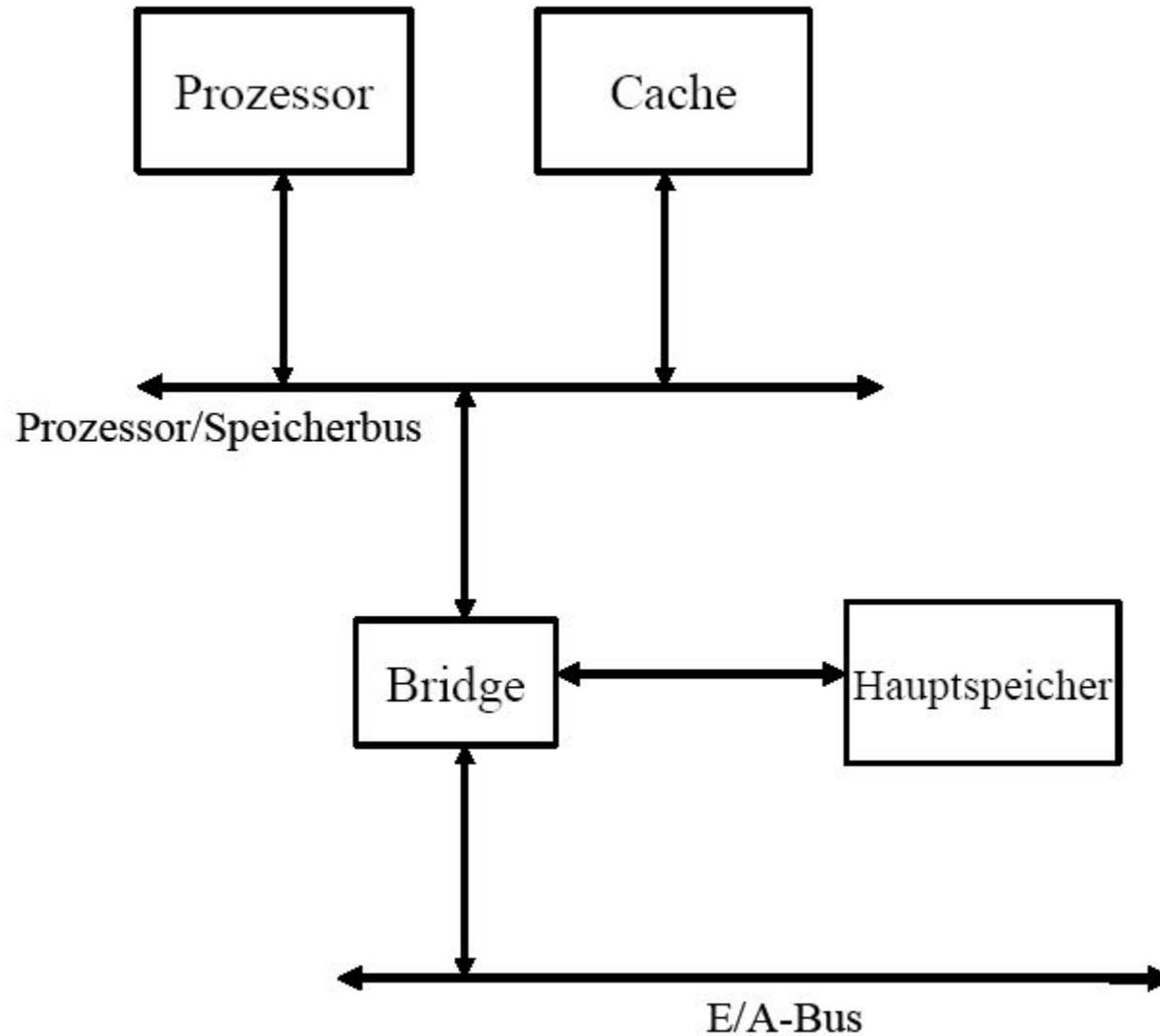
8.4 Organisation einer I/O-Einheit.



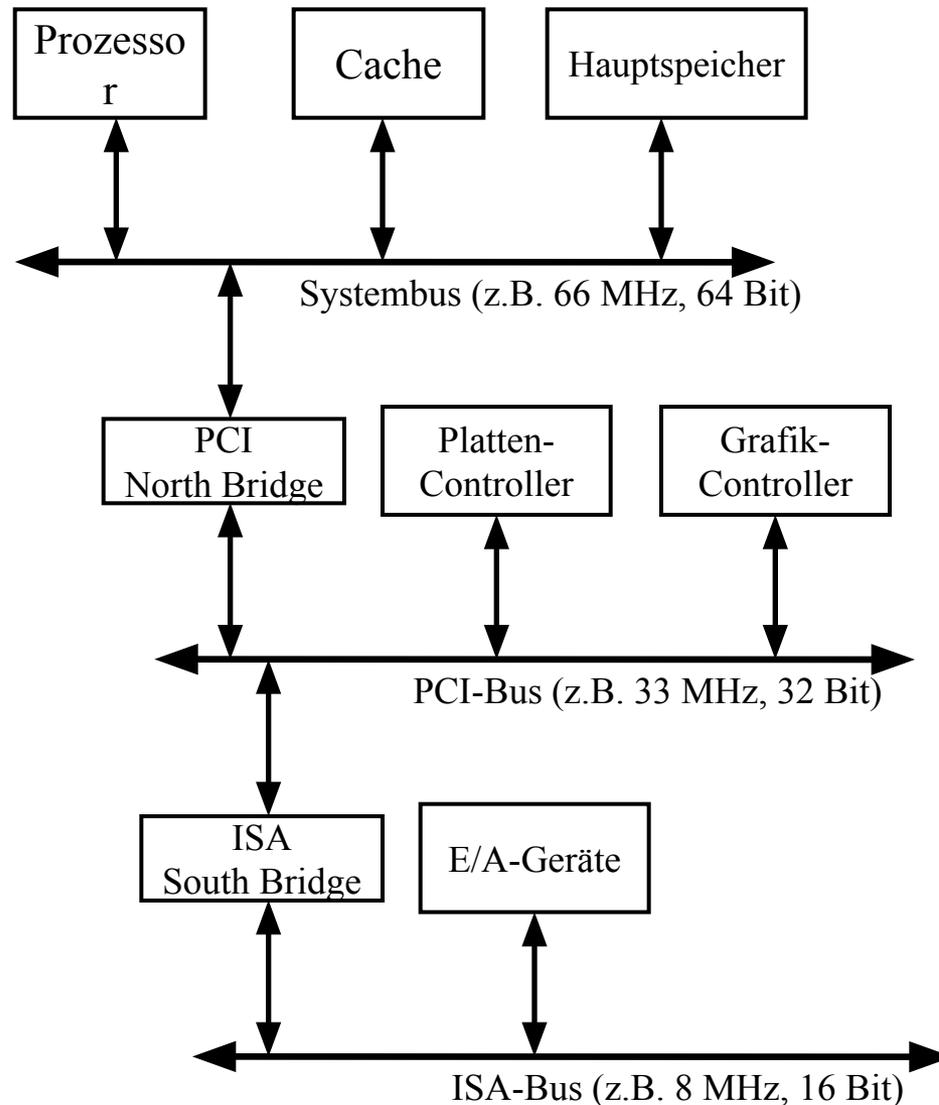
8.5 Zweibussystem.



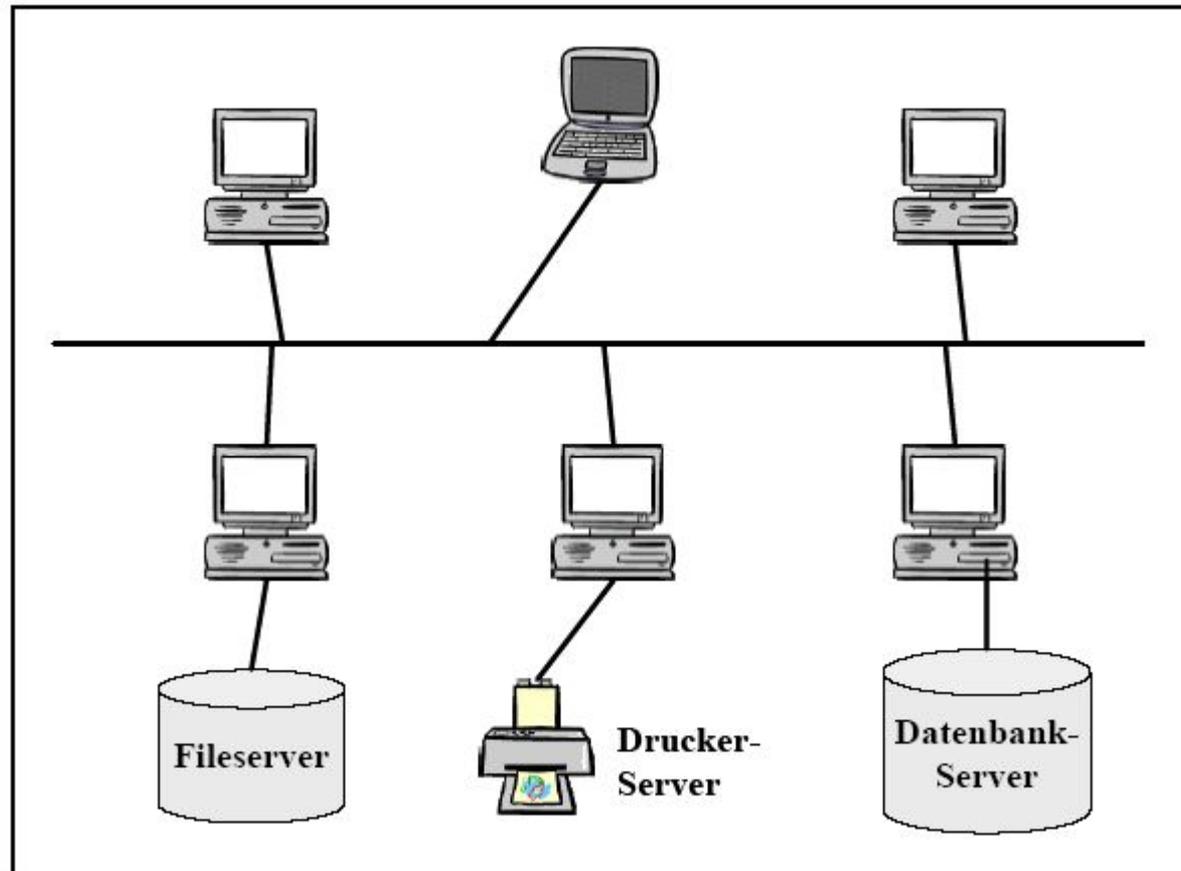
8.6 Variante des Zweibussystems.



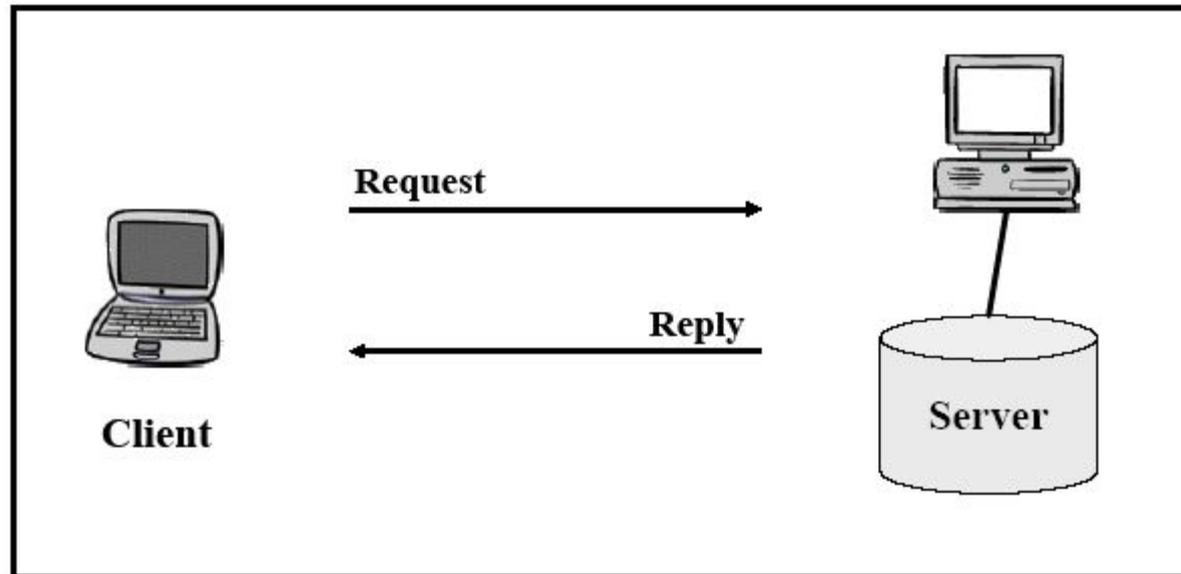
8.7 Dreibussystem mit PCI- und ISA-Bus.



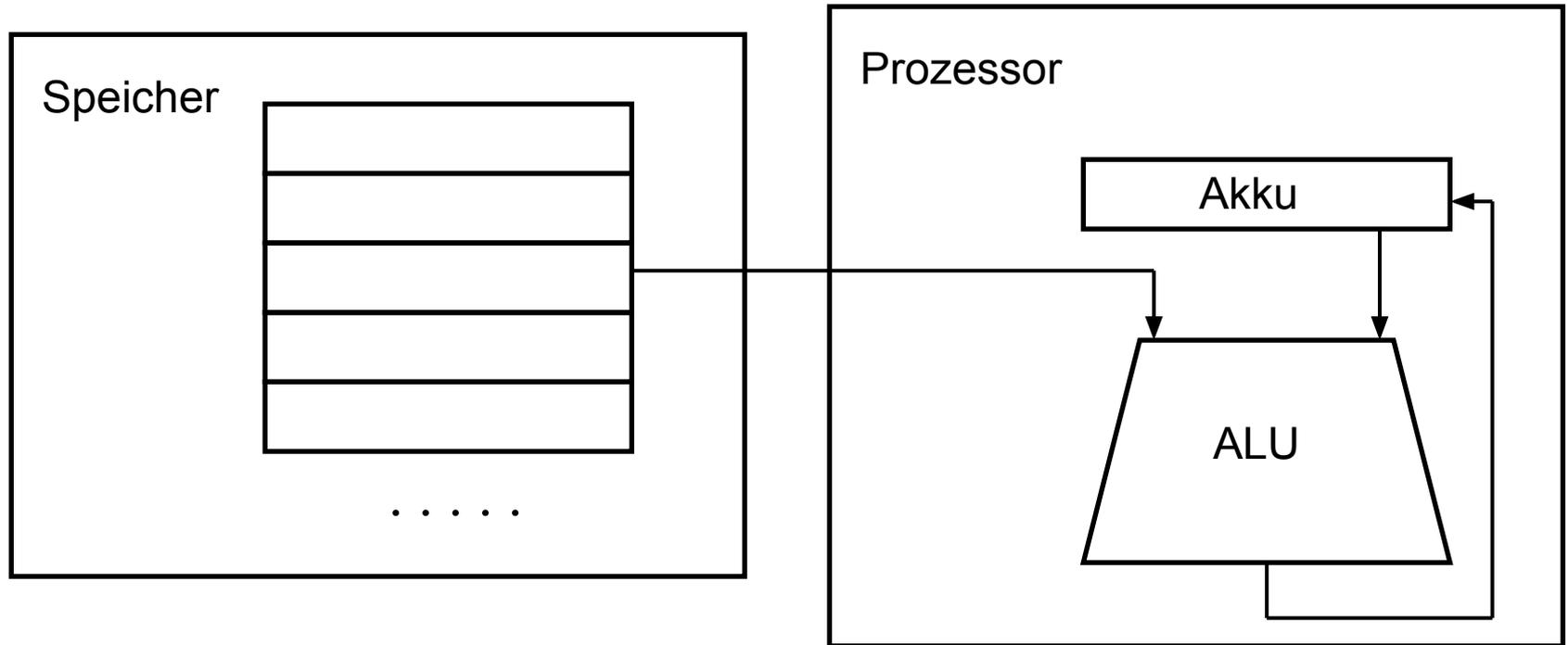
8.8 Vernetzte Rechnerkonfiguration mit Servern.



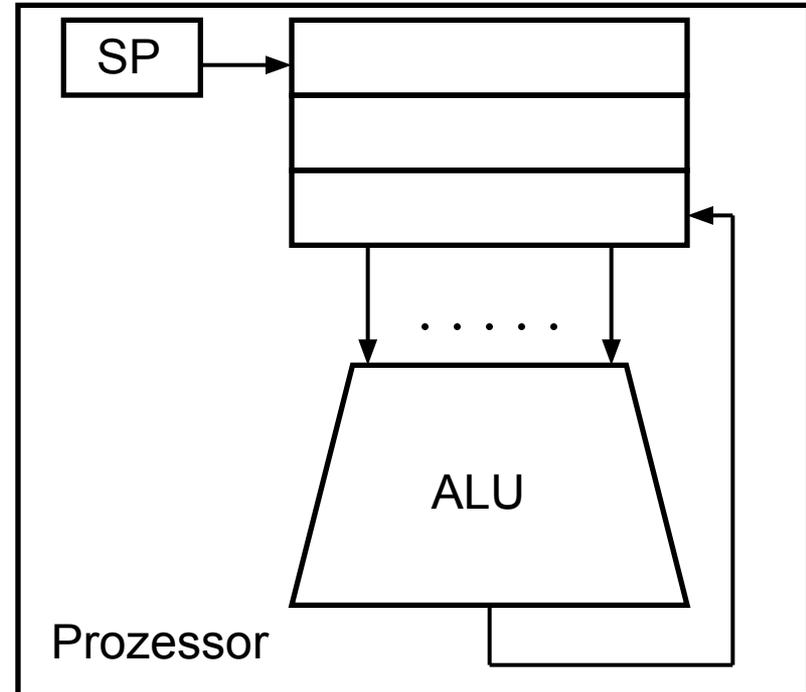
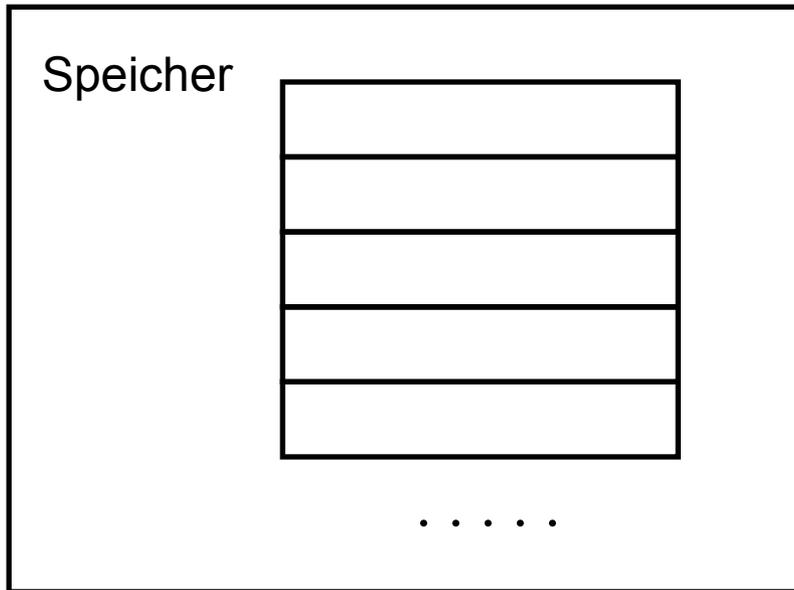
8.9 Request-Reply-Prinzip.



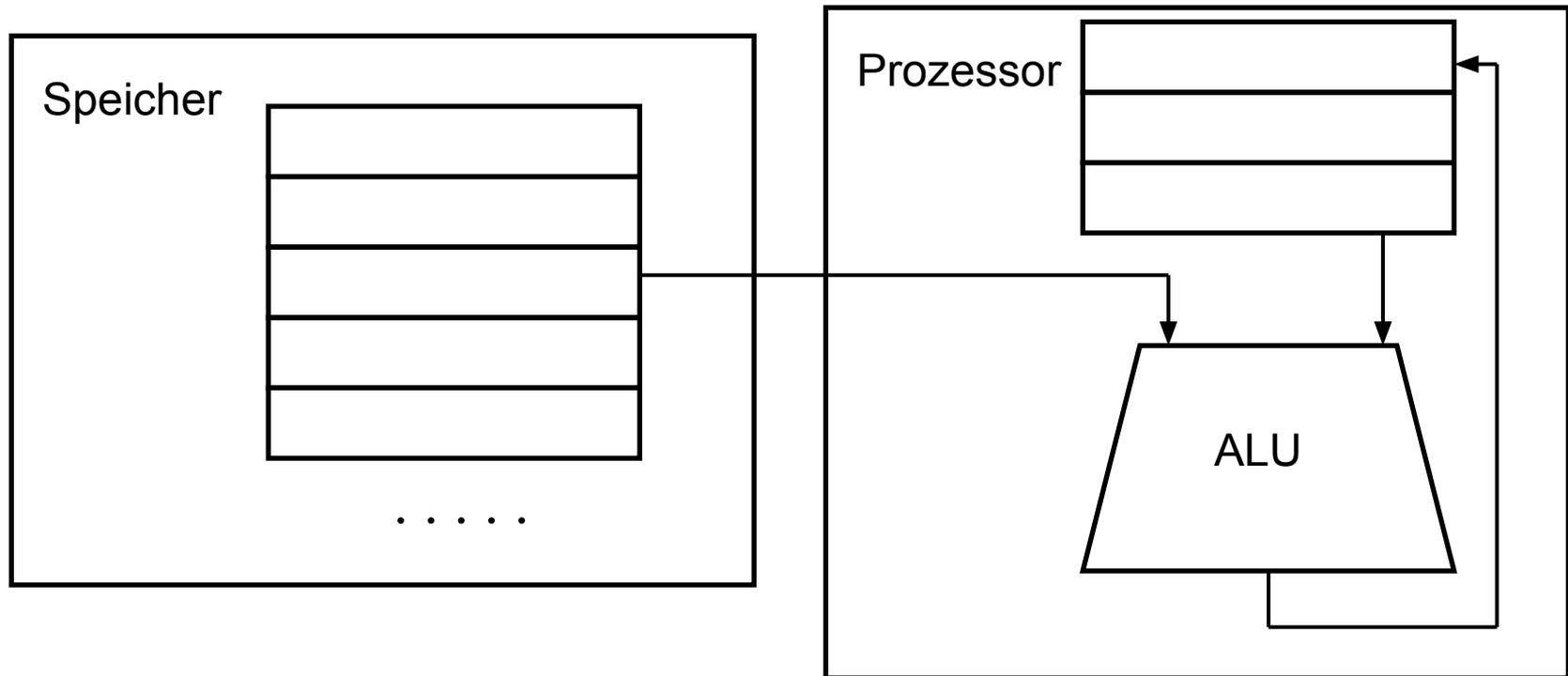
8.10 Akkumulator-Architektur.



8.11 Stack-Architektur.



8.12 Register-Speicher-Architektur.



8.13 Register-Register-Architektur.

